

Abstracts

The Design of Broadband Frequency Doublers Using Charge-Storage Diodes (1969 [MWSYM])

K.L. Kotzebue and G.L. Matthaei. "The Design of Broadband Frequency Doublers Using Charge-Storage Diodes (1969 [MWSYM])." 1969 G-MTT International Microwave Symposium Digest of Technical Papers 69.1 (1969 [MWSYM]): 136-142.

With varying degrees of approximation, the parametric action in many frequency multipliers can be viewed as the coupling of impedances at different frequencies by means of an impedance inverter. One such multiplier which can be so characterized is the charge-storage multiplier without idlers. In the equivalent circuit for such a multiplier, as shown in Fig. 1, the time-varying action of the diode is represented by the impedance inverter marked K.

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